Review of System – On- Chip Advanced eXtensible Interface (AXI) Bus Protocol

Dr. K.C. Mahajan[1], Mukesh K Yadav[2]

Professor, Technocrats Institute of Technology (Excellence), Bhopal Madhya Pradesh[1]

PhD Scholar (Electronics) Mewar University Chitodgarh, Rajasthan[2]

Abstract: **This paper gives a brief description of various on-chip bus protocols such as the Advanced Microcontroller Bus Architecture (AMBA) Advanced High-Performance bus (AHB) and Advanced Extensible Interface (AXI), Wishbone Bus, Open Core Protocol (OCP) and CoreConnect Bus. It gives a brief introduction of high performance system-on-chip bus protocol termed as an Extensible Interface (AXI). The AMBA AXI protocol supports high-performance, high-frequency system designs. It includes the optional extensions that cover signaling for low-power operation.**

Keywords: **Advanced Extensible Interface (AXI), Advanced Microcontroller Bus Architecture (AMBA), System-on-chip; Low power.**